



Avoid KTs

Why appear for the same exam twice ?

Do it once, Do it right

with Vidyalankar's Crash Course

S.E. Sem. III

Expert Faculty

Structured Study Material

Affordable Fees

Great Results

Fee	Dates / Timings (Nov. 2018)	Branches having this subject	Batches commencing on Thursday	Batches commencing on Sunday	Batches commencing on Wednesday
			8, 9, 10 Nov. 8 a.m. to 1 p.m.	11, 12, 13, 14 Nov. 8 a.m. to 1 p.m.	21, 22, 23 Nov. 8 a.m. to 1 p.m.
₹ 4,400	Maths III	CMPN, EXTC, ETRX, INST, BIOM, ELEC, MECH, AUTO, PROD, CIVIL		8011293 Dadar 8012293 Thane	8011593 Borivali 8013393 Nerul
		INFT		8321293 Dadar	
₹ 3,800	DLDA [Digital Logic Design & Analysis]	CMPN			8041295 Dadar
	ECCF [Electronic Circuits & Communication Fundamentals]	CMPN	8061292 Dadar		
	LD [Logic Design]	INFT			8081295 Dadar
	PCOM [Principle of Communications]	INFT	8101292 Dadar		
	EDC-I [Electronic Devices & Circuits I]	EXTC	8111292 Dadar		
	DSD [Digital System Design]	EXTC			8121295 Dadar
	CTN [Circuit Theory and Networks]	EXTC	8131292 Dadar		
	EDC-I [Electronic Devices & Circuits I]	ETRX	8141292 Dadar		
	DCD [Digital Circuit Design]	ETRX			8151295 Dadar
	EN/W [Electrical Network Analysis and Synthesis]	ETRX	8161292 Dadar		
	DE [Digital Electronics]	INST			8191295 Dadar
	EN/W [Electrical Networks and Measurement]	INST	8171292 Dadar		
	ECAD [Electronic Circuit Analysis and Design]	BIOM	8201292 Dadar		
	EN/W [Electrical Network Analysis and Synthesis]	BIOM	8211292 Dadar		
TD [Thermodynamics]	MECH, AUTO		8242292 Thane		

50% Discount to KT Students

50% Discount to students having KT in any subject / any semester (Submit copy of marksheet and college ID)

- Note :**
- i) Sem. III exam will commence from 20 Nov. Sem. II (KT) exam will commence from 15 Nov.
 - ii) New students (i.e. enrolling at Vidyalankar for the first time) are required to pay a one time registration charge of ₹ 300/-.
 - iii) Full amount to be paid in advance in Cash or Credit/Debit Card at the time of admission. (No Cheques Please).
 - iv) Fees once paid will not be refunded or adjusted under any circumstances.

Sequence in which the Topics will be covered in our Batches

Maths – III

[CMPN, ETRC, ETRX, INST, BIOM, ELEC, MECH, AUTO, PROD, CIVIL]

Day	Time	Topic	Topics to be covered		
			CMPN	ETRC, ETRX, INST, BIOM, ELEC	MECH, AUTO, PROD, CIVIL
1	8 a.m. to 1 p.m.	Laplace Transform, Inverse Laplace Transform	✓	✓	✓
2	8 a.m. to 1 p.m.	Complex Variable & Mapping	✓	✓	✓
		Correlation & Regression	✓		✓
3	8 a.m. to 1 p.m.	Fourier Series & Complex Form of Fourier Series	✓	✓	✓
4	8 a.m. to 1 p.m.	Vector Integral		✓	
4	1.30 p.m. to 6.30 p.m.	Complex Integration			✓

Maths – III

[INFT]

Day	Time	Topic
1	8 a.m. to 1 p.m.	Laplace Transform
2	8 a.m. to 1 p.m.	Laplace Transform & Complex Variable
3	8 a.m. to 1 p.m.	Set theory & Relation
4	8 a.m. to 1 p.m.	Function Permutations Combinations & Probability

DLDA/LD/ DSD/DCD/DE

[CMPN, INFT, ETRC, ETRX, INST]

Day	Time	Topic
1	8 a.m. to 1 p.m.	Number System and Codes
2	8 a.m. to 1 p.m.	Combinational Logic Circuit
3	8 a.m. to 1 p.m.	Sequential Logic Circuit

ECCF

[CMPN]

Day	Time	Topic
1	8 a.m. to 1 p.m.	BJT Characteristics, DC Analysis, AC Analysis, Oscillators, Power Amplifier
2	8 a.m. to 1 p.m.	OPAMP, AM, AM Sums, FM, AM Receiver
3	8 a.m. to 1 p.m.	PAM, PWM, PPM, PCM, DM, ADM Information Theory (Sums)

PCOM
[INFT]

Day	Time	Topic
1	8 a.m. to 1 p.m.	Basics of analog communication systems. Need for modulation and demodulation. AM: Amplitude modulation techniques and its types- DSBFC AM, DSBSC-AM, SSBSC, AM Receivers – Block diagram of TRF receivers and Super heterodyne receiver. Receiver characteristics
2	8 a.m. to 1 p.m.	FM : Principle of FM Preemphasis and de-emphasis in FM, FM noise triangle, Comparison of AM and FM systems, FM generation: Direct method and Indirect, FM demodulator. Sampling theorem for low pass and band pass signals with proof, Anti- aliasing filter, PAM, PWM and PPM generation.
3	8 a.m. to 1 p.m.	Introduction to digital communication (Block diagram), Quantization process, Pulse code Modulation, DM, ADM, TDM and FDM. Introduction to Line codes, Inter symbol interference, BPSK, DEPSK, QPSK, M-ary PSK, QASK

EDC-I/ECAD
[EXTC, ETRX, BIOM]

Day	Time	Topic
1	8 a.m. to 1 p.m.	BJT Working & Biasing BJT Amplifier Analysis
2	8 a.m. to 1 p.m.	JFET Amplifier Analysis Low & High Frequency Analysis of CE & CS Amplifier
3	8 a.m. to 1 p.m.	BJT & JFET Amplifier Designing Power Supply Designing

CTN/ENW
[EXTC, ETRX, INST, BIOM]

Day	Time	Topic
1	8 a.m. to 1 p.m.	Network Analysis / Circuit Analysis Two-Port Network Analysis
2	8 a.m. to 1 p.m.	Transient Analysis of Networks Laplace Transformation of Network
3	8 a.m. to 1 p.m.	Representation of Network Functions Network Synthesis

Thermodynamics
[MECH, AUTO]

Day	Time	Topic
1	8 a.m. to 1 p.m.	Basic concepts & Definitions First Law of Thermodynamics
2	8 a.m. to 1 p.m.	Second Law of Thermodynamics Thermodynamic Relations , Availability Properties of Pure Substance
3	8 a.m. to 1 p.m.	Compressors, Vapour Power Cycles Gas Power Cycles

Vacation Batches AFTER Sem. III Exams for Sem. IV subjects :

Prepare the subjects of the next Sem. in advance (i.e. even before the opening of the college).
(Details of our Sem. IV Vacation batches can be had from our office after 20 Nov. '18)

Corporate Office: Pearl Centre, S. B. Marg, Dadar (W), Mumbai – 400 028

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• Ghatkopar – 4232 24 00 / 2512 90 28 • Thane – 4232 22 00 / 2544 33 19 • Dombivli – 0251-248 03 21 • Kalyan – 0251-220 18 60 / 70
• Chembur – 2528 31 62 / 2523 41 81 • Vashi – 4173 32 00 / 2789 31 85 • Nerul – 2770 26 39 / 76 95 • Panvel – 2745 99 66 / 2745 42 32

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Skill Development Courses @ Vidyalankar

Today's Industry demands Skills, and only theoretical technical knowledge is not sufficient. Skills are imparted by expert tutor and in-depth hands-on training. The skill development courses at Vidyalankar are specially designed to satisfy these needs. We are coming up with following courses :

➤ COURSE NAME : PYTHON PROGRAMMING & DATA SCIENCE

Duration : 40 Hrs. (10 Sessions of 4 Hrs.)

Fee : ₹ 4,500/-

Center	Time	Commencement Date
Dadar	9 a.m. to 1 p.m.	19 Dec. 2018
Thane	2:30 p.m. to 6:30 p.m.	19 Dec. 2018
Borivali	2:30 p.m. to 6:30 p.m.	19 Dec. 2018

Contents : Introduction, Data Types, Special Data Types, Tuples, Sets Dictionaries, Control Structures & Functions – If, For, While loops, Classes, constructors, methods, modules, packages, Files, File handling, Text files, Binary files, Writing GUI in Python, databases in python, mysql with python, Python and Data Science, using NumPy, SciPy, drawing plots and graphs with Matplotlib.

Note: Students need to carry their own laptop for the sessions (Rest of the setup would be provided at Vidyalankar)

➤ COURSE NAME : EMBEDDED C PROGRAMMING WITH ARDUINO & RASPBERRY-PI

Duration : 36 Hrs. (9 Sessions of 4 Hrs.)

Fee : ₹ 4,500/-

Center	Time	Commencement Date
Dadar	9 a.m. to 1 p.m.	26 Dec. 2018
Thane	2:30 p.m. to 6:30 p.m.	26 Dec. 2018

Contents : Introduction to Embedded Systems, Sensors, Arduino environment, Arduino IDE, Basic programming with interfaces Buzzer, LED, Relay; Module Interfacing – ESP 8266 (WiFi), XC05 (Bluetooth) DHT, Gas Sensor, IoT application development - Working with Thing-speak cloud. Introduction to Raspberry-Pi, Protocols, Raspbian OS install, Python Scripting for R-Pi, GPIO, ports, SD-Card, Sensor integration, IoT applications.

Note: Students need to carry their own laptop for the sessions (Rest of the setup would be provided at Vidyalankar)

Coming Soon : Advanced Python, Android Development, iOS development.

Seminar on "Opportunities through GATE – 2021" for Students moving to SE sem. IV

Date	Day	Center	Time
19 Dec. 2018	Wednesday	Vashi	10:30 a.m.
		Ghatkopar	
20 Dec. 2018	Thursday	Kalyan	
		Panvel	
21 Dec. 2018	Friday	Andheri	
		Chembur	
22 Dec. 2018	Saturday	Borivali	
		Thane	
23 Dec. 2018	Sunday	Nerul	
		Dadar	
24 Dec. 2018	Monday	Dombivli	
		Kandivali	